Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (canceled)

2. (previously presented) The image display device according to claim 5, wherein both ends of the dummy line are not connected to other signal lines.

Claim 3 (canceled)

4. (previously presented) The image display device according to claim 5, wherein the dummy line is constituted of a plurality of lines which are arranged in parallel.

5. (currently amended) An image display device is characterized in that a drive circuit which supplies signals to respective pixels in an image display part of a substrate through signal lines arranged in a plane of the substrate is formed outside the image display part,

the drive circuit is constituted of a plurality of semiconductor devices, and the respective semiconductor devices are configured such that data is supplied between these respective semiconductor devices and other semiconductor devices, which are arranged adjacent to these respective semiconductor devices, through data transfer signal lines arranged in the plane of the signal lines of the substrate, and

a dummy line arranged in both of the plane of the signal lines of the substrate and in the plane of the data transfer lines of the substrate is formed in the plane of the substrate between the signal lines in the plane of the substrate and the data transfer signal lines in the plane of the substrate;

wherein the dummy line is formed so as to extend along at least one of the signal ~~line~~ lines in the plane of the substrate.

6. (original) The image display device according to claim 5, wherein the signal lines are drain signal lines which supply video signals to respective pixels, and the drive circuit constitutes a video signal drive circuit.

7. (original) The image display device according to claim 5, wherein the signal lines are gate signal lines which supply scanning signals to respective pixels, and the drive circuit constitutes a scanning signal drive circuit.

8. (previously presented) The image display device according to claim 5, wherein signal lines which are arranged adjacent to each other are formed into groups,

the signal lines which are formed into each group are directed in a converging direction outside the image display part and are connected to respective semiconductor devices, and data transfer signal lines which connect between one semiconductor device and another semiconductor device that is arranged adjacent to the one semiconductor device are formed such that the data transfer signal lines loop around an area at the image display part side between these respective semiconductor devices.

9. (currently amended) The image display device according to claim 5, wherein the dummy ~~lines are~~ line is connected with at least one of the signal lines which are arranged adjacent to the dummy ~~lines~~ line.

10. (currently amended) The image display device according to claim 9, wherein the connection between the dummy ~~lines~~ line and the signal lines ~~are~~ is formed into a seal material which seals a pair of substrates.

11. (currently amended) An image display device is characterized in that a pair of electrodes are formed on each pixel within an image display part of a substrate, one of the pair of electrodes includes a counter electrode to which a counter voltage supply signal which becomes a reference with respect to signals supplied to another electrode of the pair of electrodes is supplied,

a drive circuit which supplies signals to the respective pixels through signal lines arranged in a plane of the substrate is formed outside the pixel display part, the drive circuit is constituted of a plurality of semiconductor devices, a counter voltage signal line arranged in the plane of the signal lines of the substrate which supplies counter voltage signals to the counter electrode is formed on a region between one semiconductor device and another semiconductor device which is arranged adjacent to the one semiconductor device, and

a dummy line is arranged in both of the plane of the signal lines of the substrate and in the plane of the counter voltage signal line of the substrate and between the signal lines in the plane of the substrate and the counter voltage signal line in the plane of the substrate outside of the image display part;

wherein the dummy line is formed in the plane of the substrate along with both the signal lines in the plane of the substrate and the counter voltage signal line in the plane of the substrate so as to extend along at least one of the signal lines.

12. (previously presented) The image display device according to claim 5, wherein the dummy line is formed into a seal material which seals a pair of substrates.

13. (previously presented) The image display device according to claim 5, wherein the dummy line is arranged between the signal lines and the data transfer line so as to enable prevention of a disconnection due to static electricity caused by a spark generated between one of the signal lines and one of the data transfer lines.

14. (previously presented) The image display device according to claim 5, wherein the signal lines have a bent portion along the extension thereof, and the dummy line extends along the signal lines and has a corresponding bent portion.

Claim 15 (canceled)

16. (previously presented) The image display device according to claim 11, wherein the dummy line is formed into a seal material which seals a pair of substrates.

17. (previously presented) The image display device according to claim 11, wherein the signal lines are drain signal lines which supply video signals to respective pixels.

18. (currently amended) The image display device according to claim 11, wherein the dummy line is arranged in the plane of the substrate between the signal lines in the plane of the substrate and the counter voltage ~~supply~~ signal line in the plane of the substrate so as to enable prevention of a disconnection due to static electricity caused by a spark generated between one of the signal lines and the counter voltage ~~supply~~ signal line.

19. (previously presented) The image display device according to claim 11, wherein the signal lines have a bent portion along the extension thereof, and the dummy line extends along the signal lines and has a corresponding bent portion.

20. (previously presented) The image display device according to claim 5, wherein the dummy line includes a first dummy line part and a second dummy line part, the first dummy line part is connected with the signal lines which are arranged adjacent to the first dummy line part, and the second dummy line part is connected with the first dummy line part which is arranged adjacent to the second dummy line part.

21. (previously presented) The image display device according to claim 20, wherein the connection between the first dummy line part and the signal lines, and

the connection between the second dummy line part and the first dummy line part are formed into a seal material which seals a pair of the substrates.

22. (previously presented) The image display device according to claim 20, wherein the connection between the first dummy line part and the signal lines are formed into a seal material which seals a pair of the substrates, and the connection between the first dummy line part and the second dummy line part is formed at a position where the pair of the substrates overlap each other.

23. (previously presented) The image display device according to claim 11, wherein a contact hole us formed in a region of the counter voltage signal line, and the contact hole connects the counter electrode to the counter voltage signal line.